



Patent Docket No. 35640/36899
PATENT

BARNES & THORNBURG CUSTOMER NO:

23646

U.S. PATENT AND TRADEMARK OFFICE

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

NOV 23 2004

Applicant(s): Matsumoto Toshiyuki et al. Conf. No.: 2544
Serial No.: 09/703,845 Art Unit: 2829
Filed: November 2, 2000 Examiner: Nguyen, Tung X.
For: CAPACITANCE MEASUREMENT METHOD OF MICRO
STRUCTURES OF INTEGRATED CIRCUITS

PETITION PURSUANT TO 37 C.F.R. § 1.136(a)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Petitioner, Applicant in the above-identified application, hereby requests that the response to the outstanding Office Action submitted herewith be accepted pursuant to the provisions of 37 C.F.R. § 1.136(a). The outstanding Office Action was issued on May 25, 2004, the initial period for a response expiring August 25, 2004.

As required, the appropriate fee of \$980.00 specified in 37 C.F.R. § 1.17 for a three-month extension is submitted herewith. Please credit any overpayments or charge any additional fees to the Deposit Account of Barnes & Thornburg, Account Number 02-1010 (35640/36899).

The requirements of the Code of Federal Regulations having been met, acceptance of the accompanying document is respectfully requested.

Respectfully submitted,

BARNES & THORNBURG LLP

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Enclosure